

IN THE SPECIFICATION:

Please revise the specification as follows:

Please amend paragraph number [0010] as follows:

[0010] Thus, in one aspect, the present invention provides a memory system operating in either a first mode or a second mode of operation. The memory system includes a memory controller that is connected to memory devices via at least one channel. The memory controller communicates at least one command to each one of memory devices via the at least one channel. While the memory system operates in the first mode, one of the memory devices responds to the at least one command to accomplish transfer of data between the one memory device and the memory controller during a first time period. And while the memory system operates in the second mode, a plurality of the memory devices responds to the at least one command to accomplish transfer of data between the plurality of memory devices and the memory controller during the first time period.